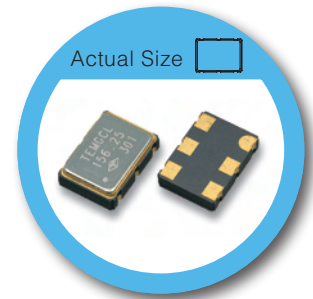


7.0 x 5.0 mm SMD PECL/LVDS Crystal Oscillator – OT Type Multiplier



RoHS Compliant Standard

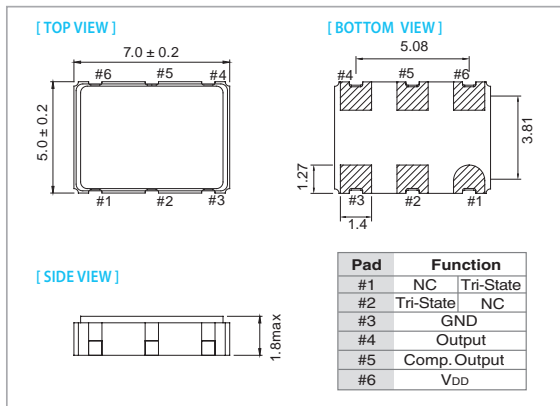
FEATURE

- Typical 7.0 x 5.0 x 1.6 mm 6 pads ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- High frequency up to 700 MHz.
- Complementary output.
- Packing: Tape & Reel, 1000/3000pcs per Reel.

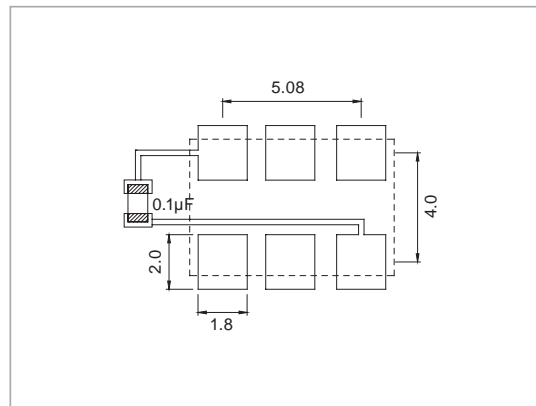
TYPICAL APPLICATION

- 10G-BIT, Ethernet, MAN, SONET
- WLAN/WiMax, xDSL
- Fiber Channel

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	PECL 3.3 V		LVDS 3.3 V		unit
	Min.	Max.	Min.	Max.	
Supply Voltage Variation (V _{DD}) 10%	2.97	3.63	2.97	3.63	V
Frequency Range	100	700	100	700	MHz
Standard Frequency	200, 250, 312.5, 622.08				
Supply Current					
100 MHz ≤ F _o < 160 MHz	–	75	–	65	mA
160 MHz ≤ F _o < 700 MHz	–	100	–	80	
Output Level					
Output High (Logic “1”)	2.275	–	–	1.6	V
Output Low (Logic “0”)	–	1.68	0.9	–	
Transition Time: Rise/Fall Time+	–	1.0	–	1.0	nSec
Start Time	–	3	–	3	mSec
Tri-State(Input to Pin 2 or Pin 1)					
Enable	0.7 V _{DD}	–	0.7 V _{DD}	–	V
Disable	–	0.3 V _{DD}	–	0.3 V _{DD}	
RMS Phase Jitter (Integrated 12 KHz - 20 MHz)	–	4	–	4	pSec
Aging	–	±3	–	±3	ppm
Storage Temp. Range	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

+ Transition times are measured between 20% and 80% of V_{DD}.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	ppm	±25	±50
-10 ~ +60	○	○	○
-20 ~ +70	△	○	○
-40 ~ +85	×	○	○

* ○: Available △: Conditional X: Not available

* Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration